

Do not enter

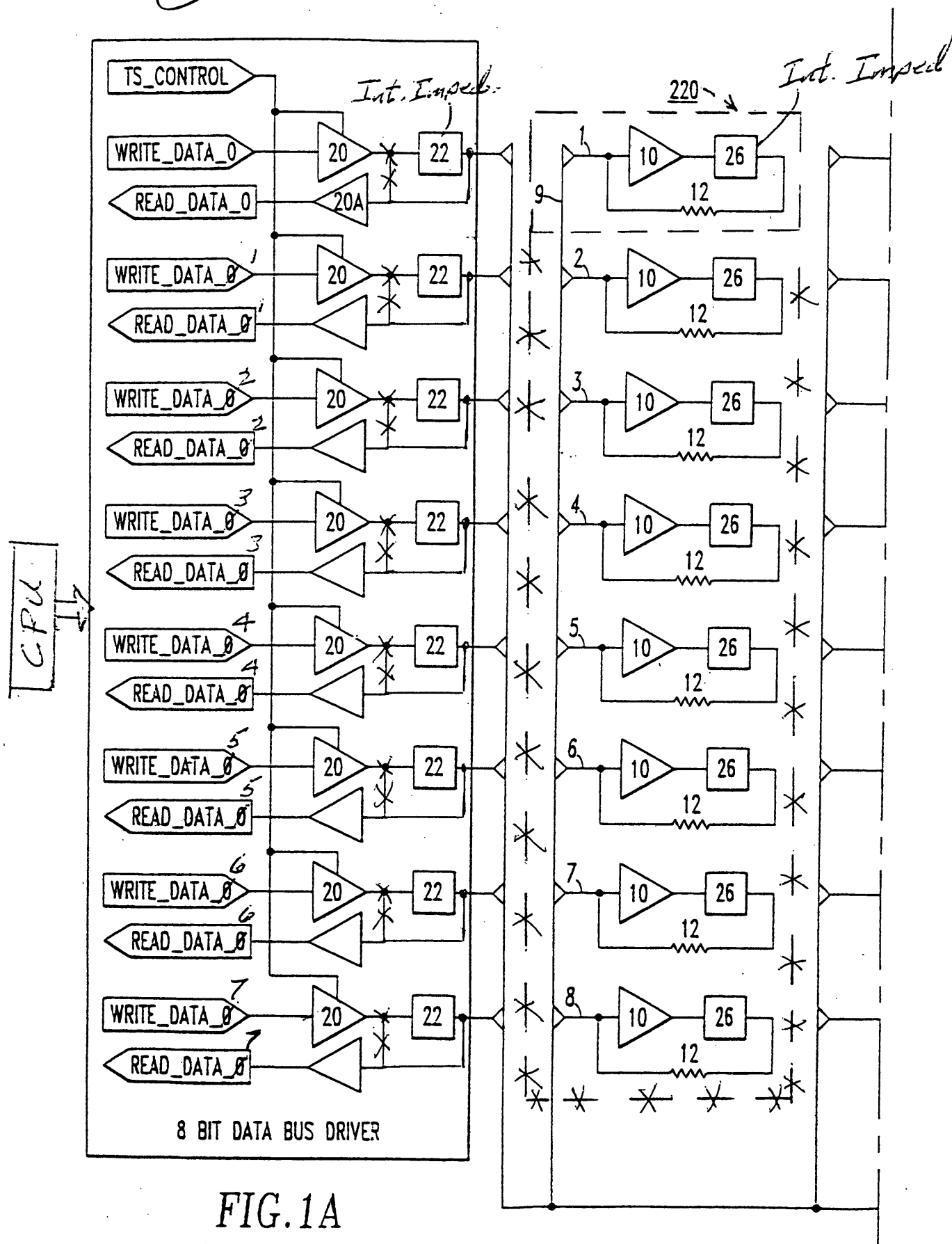
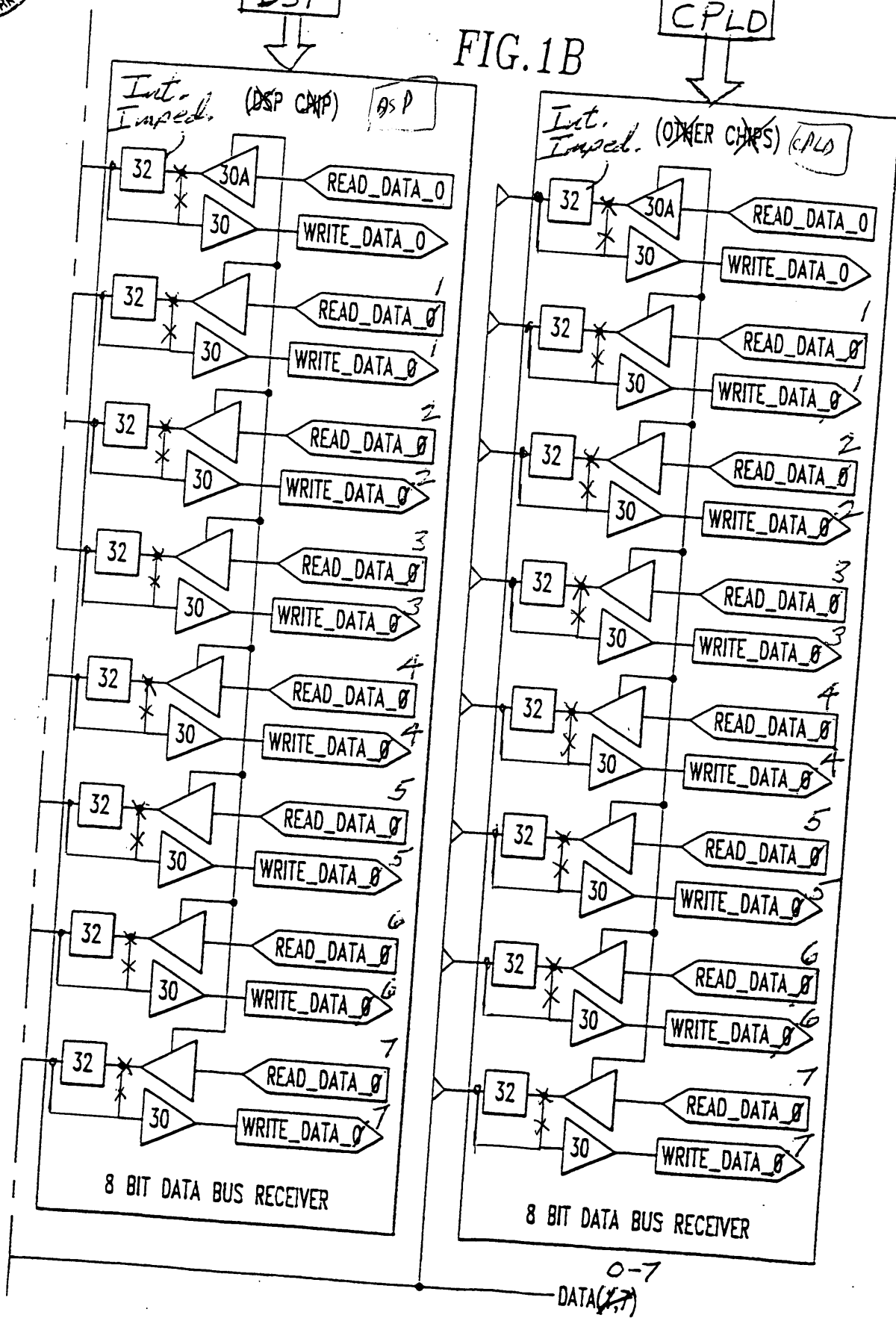


FIG.1A

DSP

FIG. 1B

CPLD



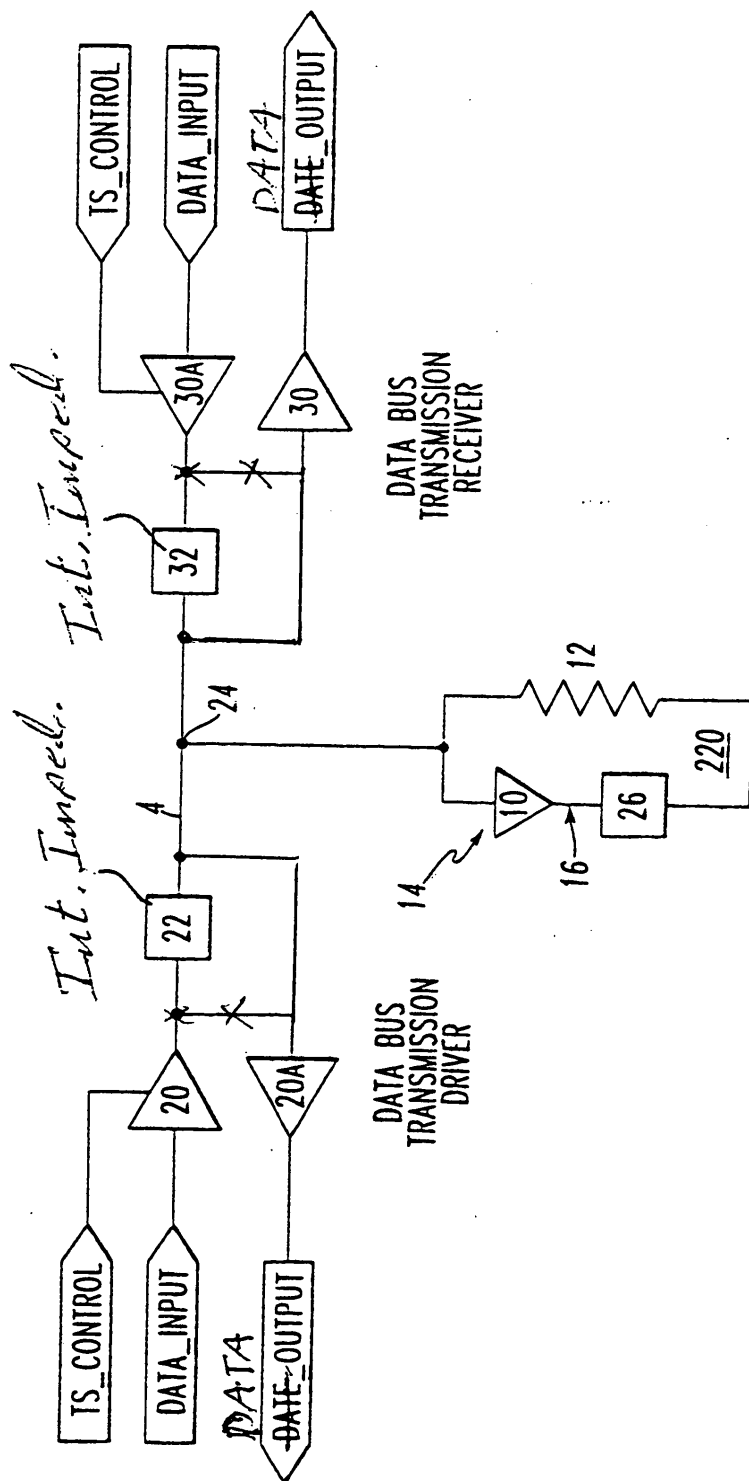
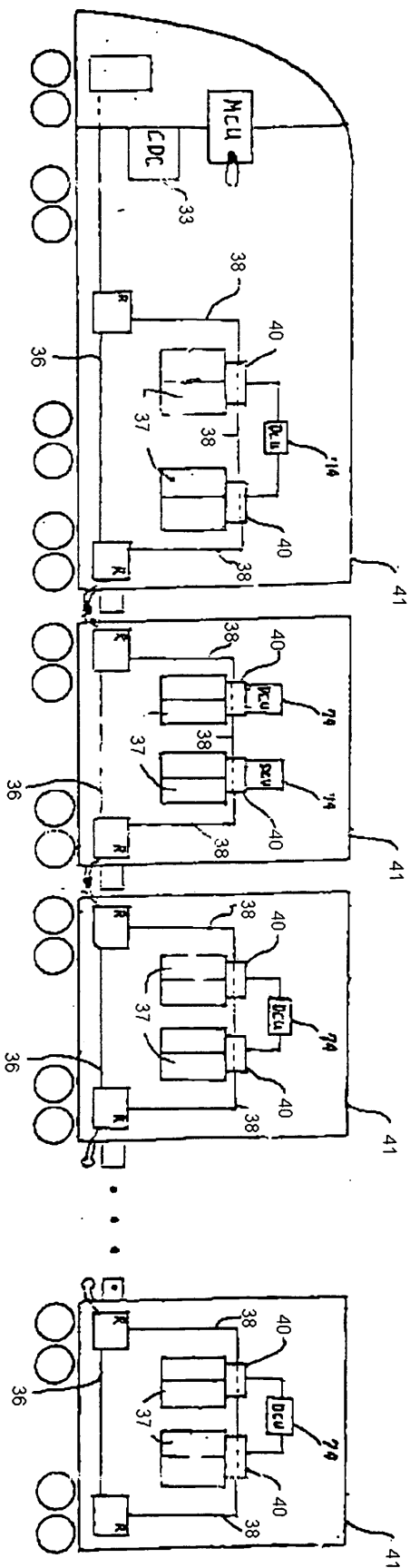


FIG. 2

FIGURE 3A
(Prior Art)



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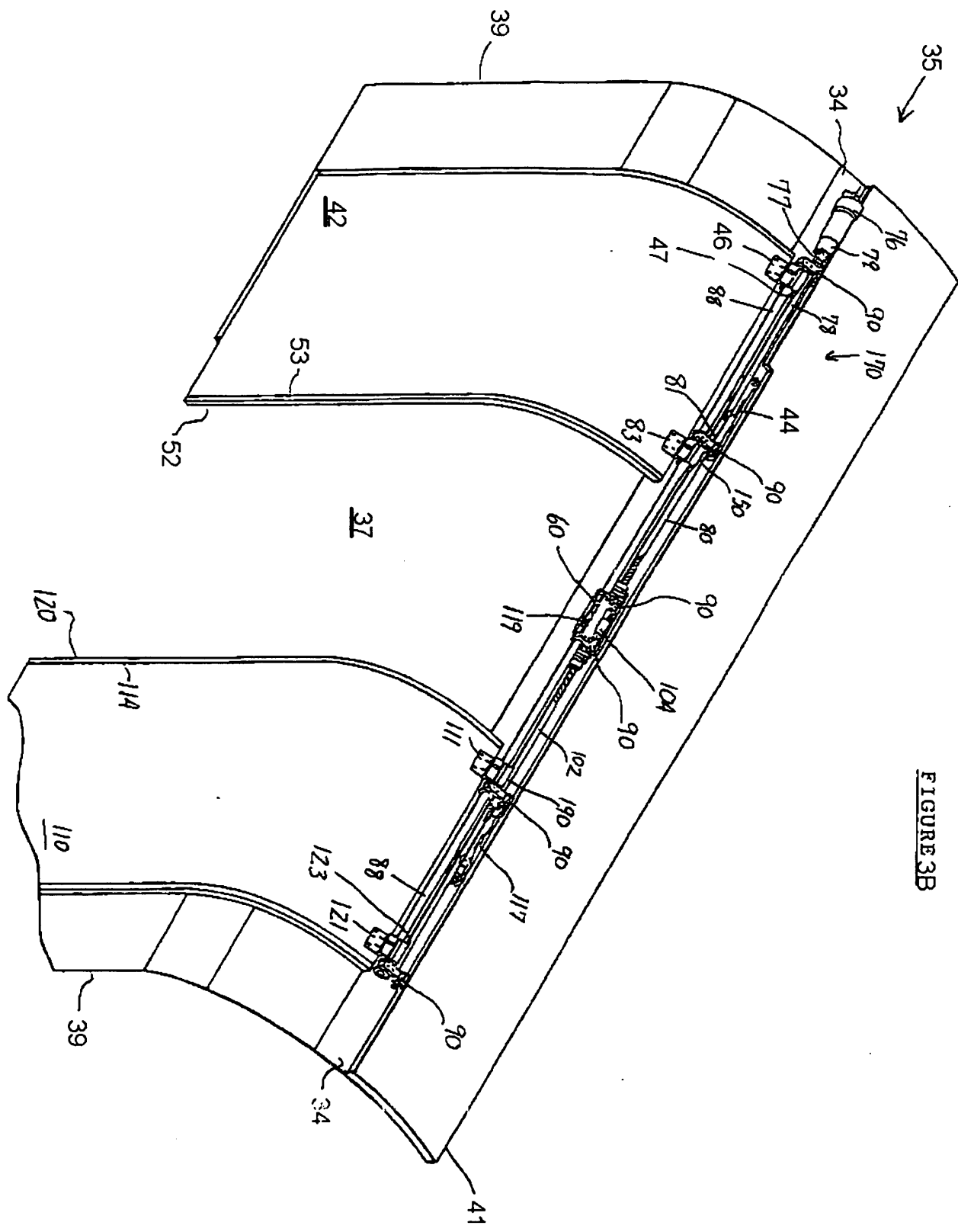


FIGURE 3B

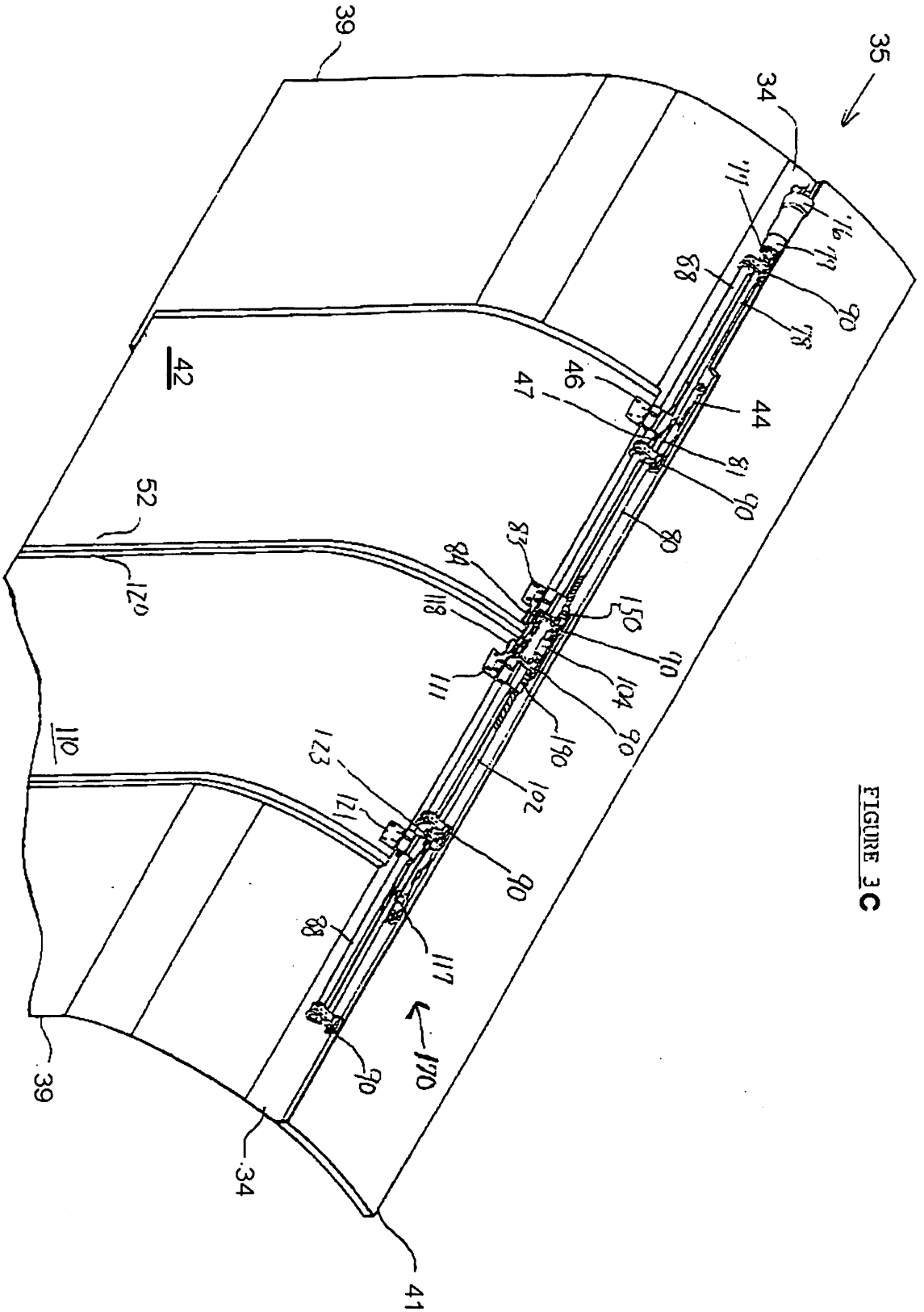


FIGURE 3C

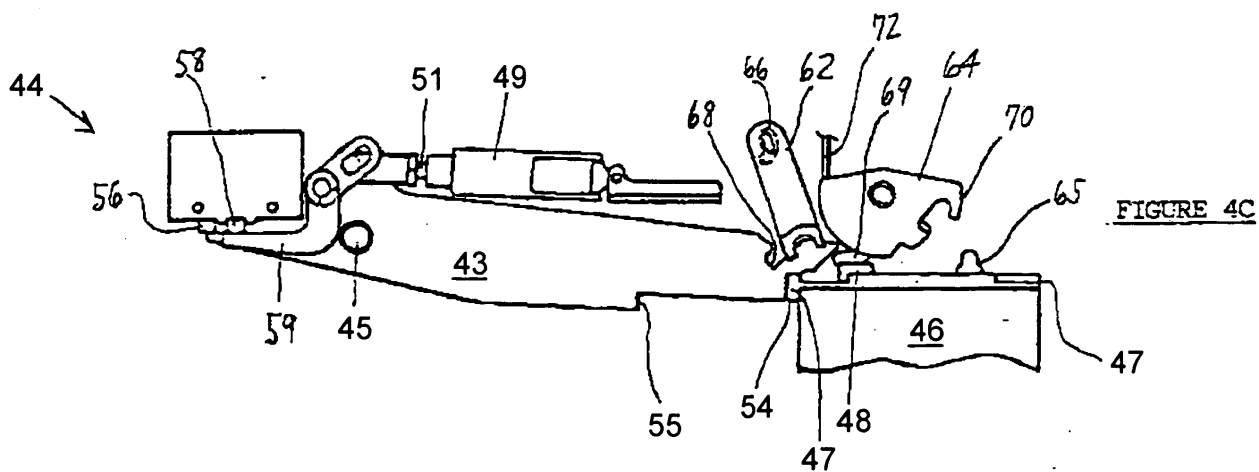
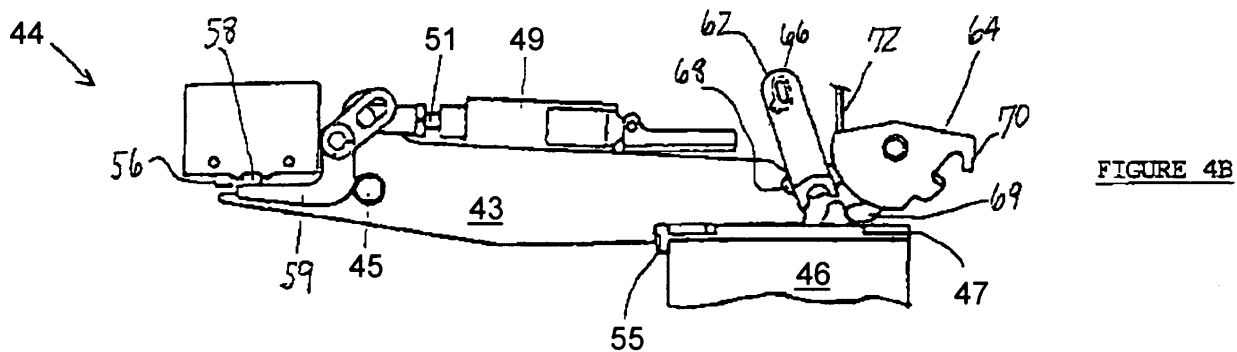
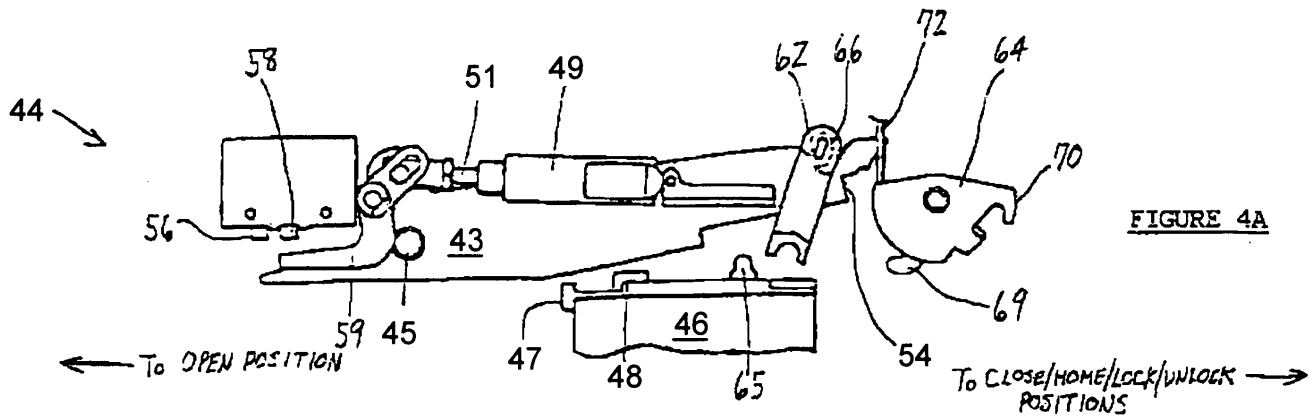


FIGURE 5A

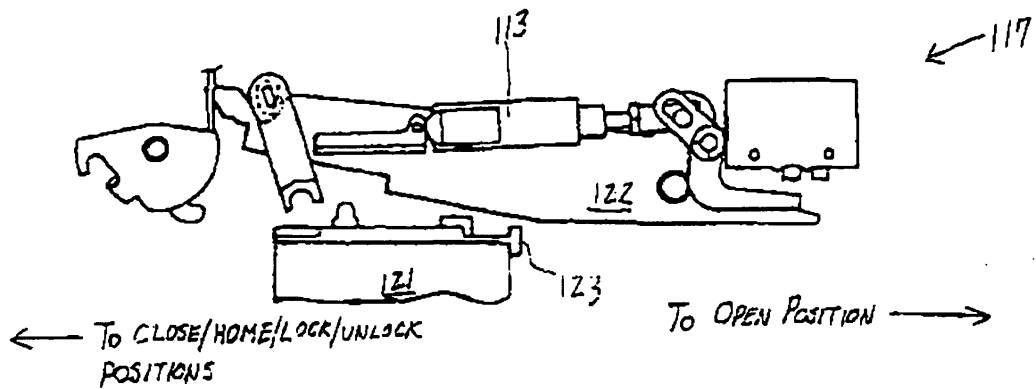


FIGURE 5B

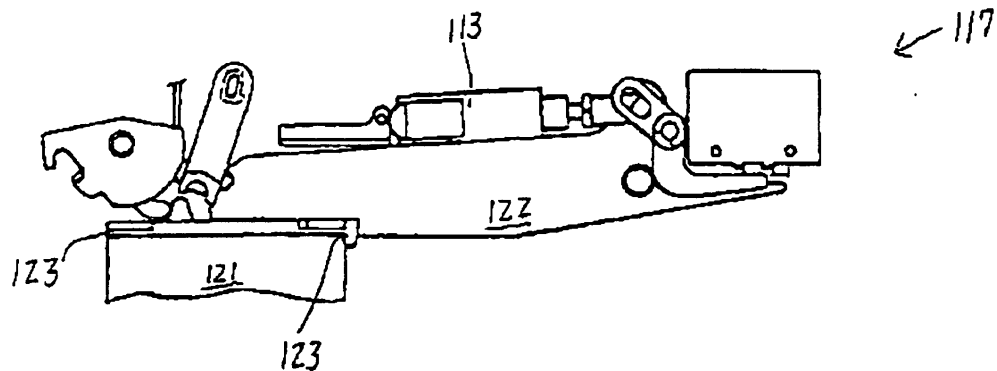


FIGURE 5C

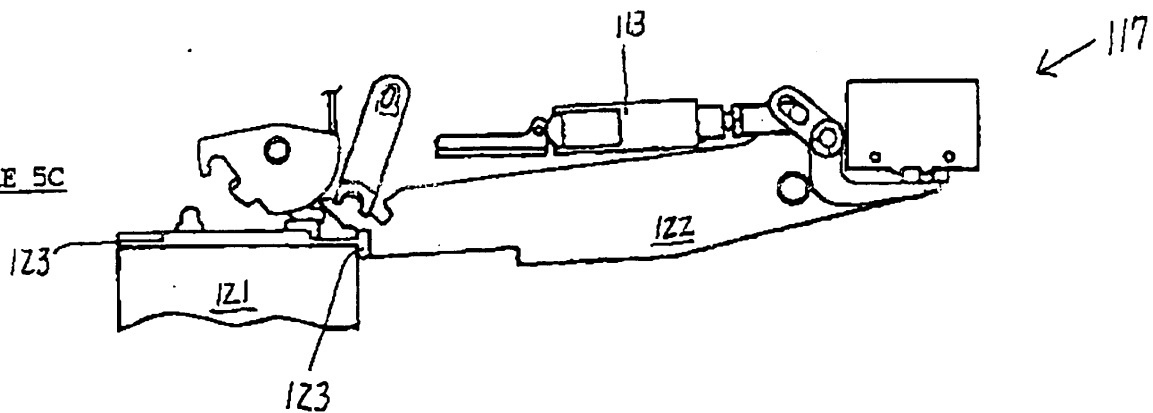






FIGURE 7B

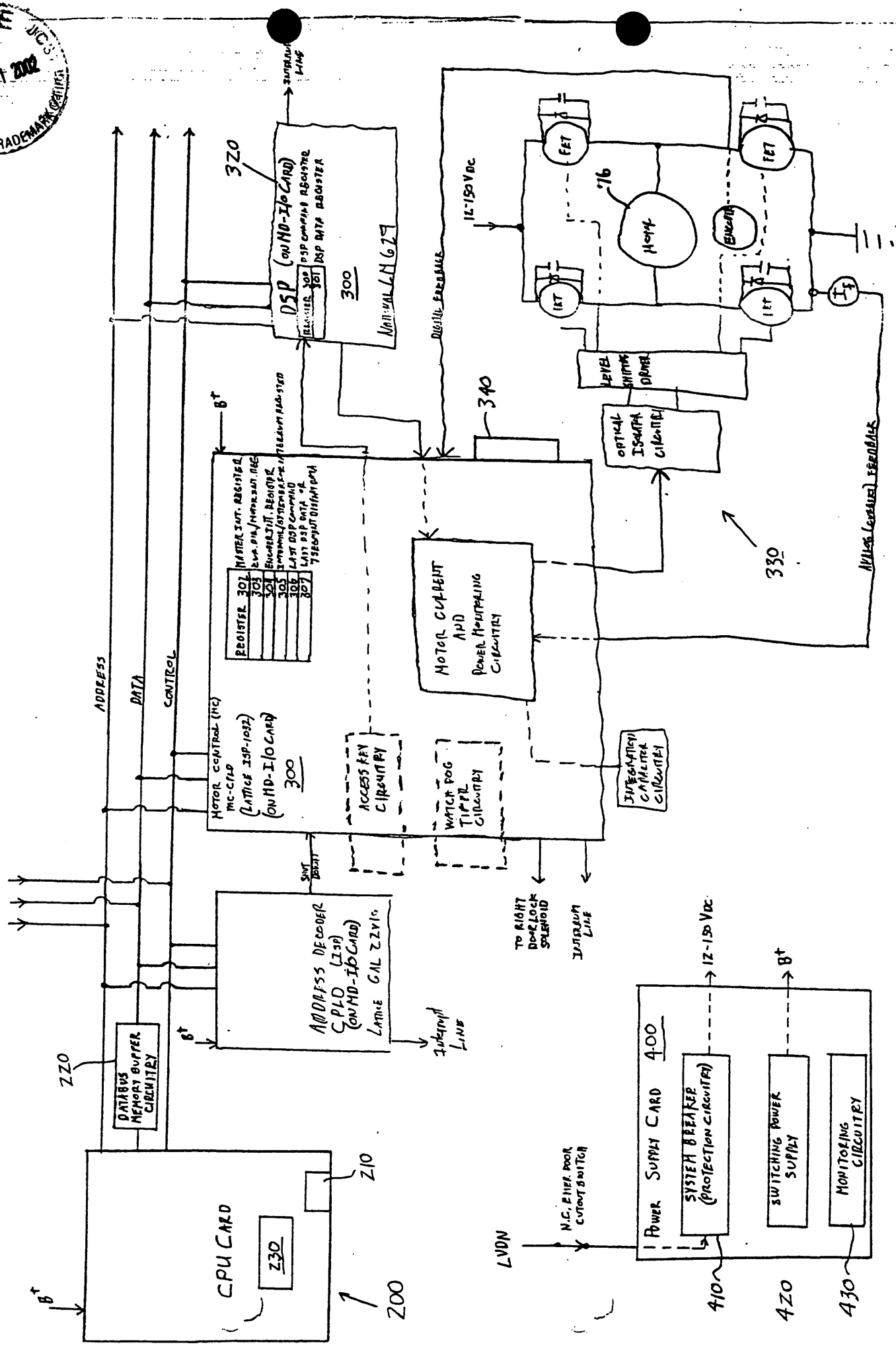
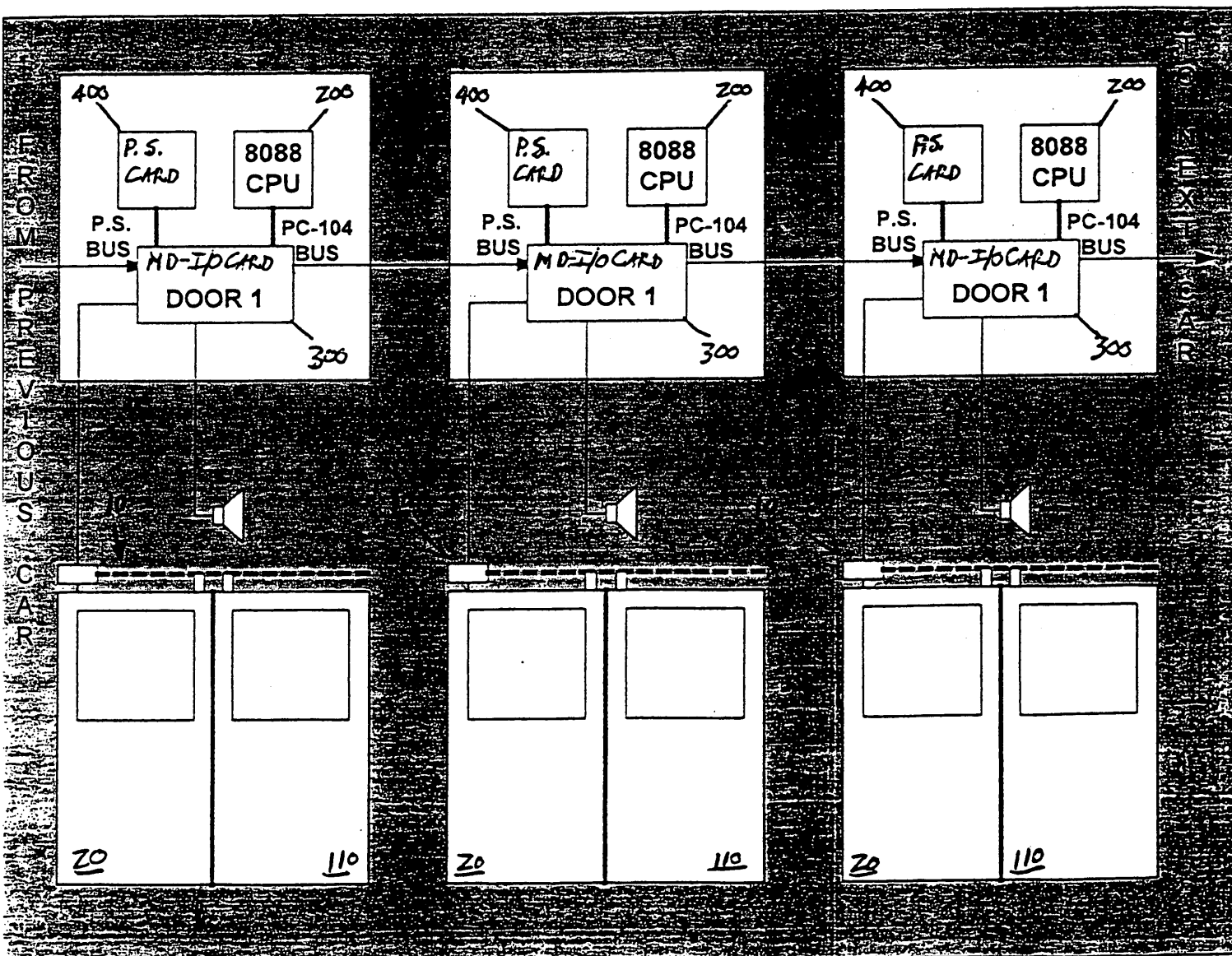


FIGURE 8



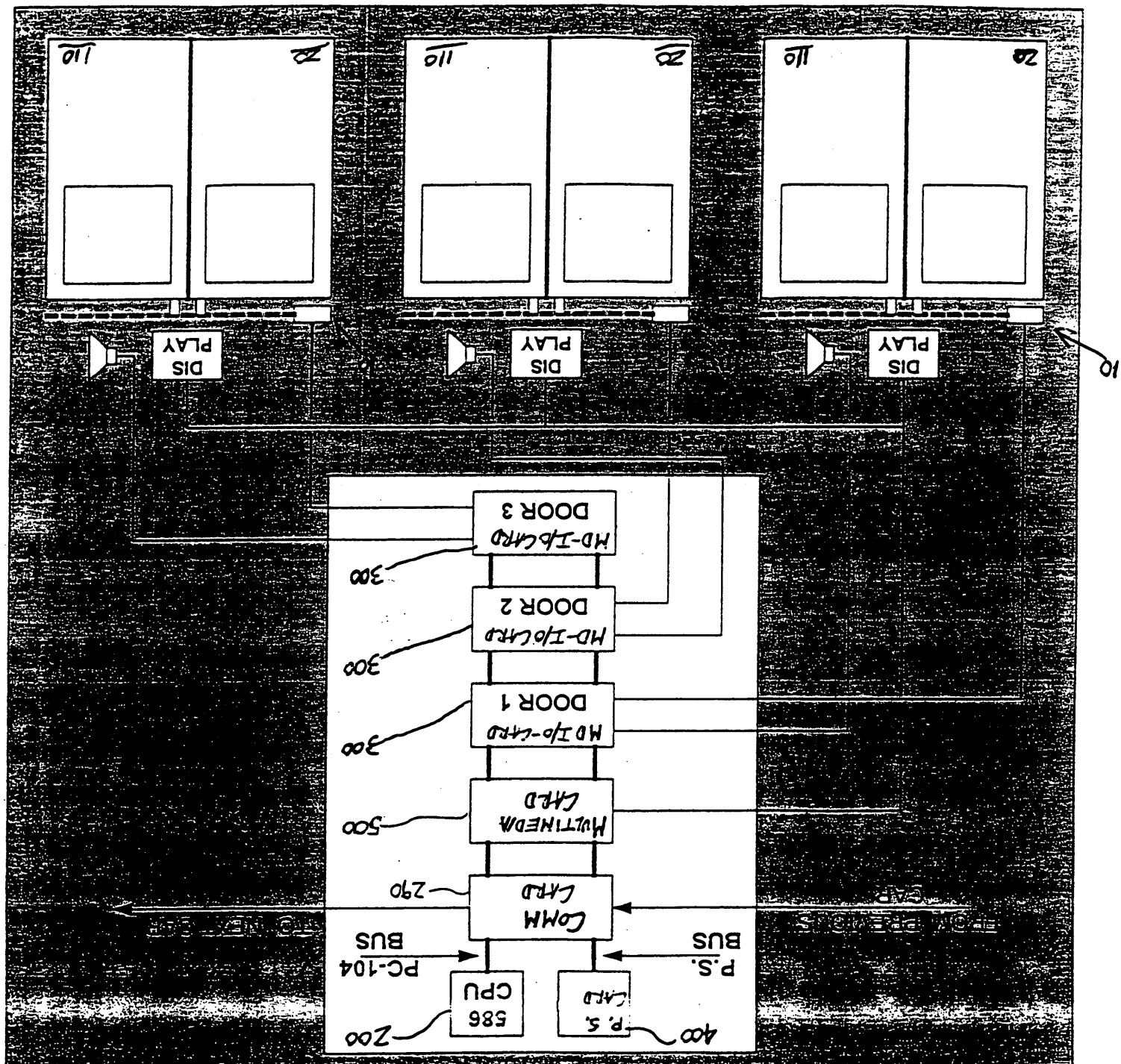
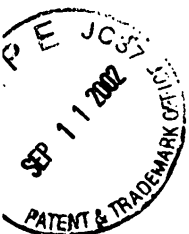


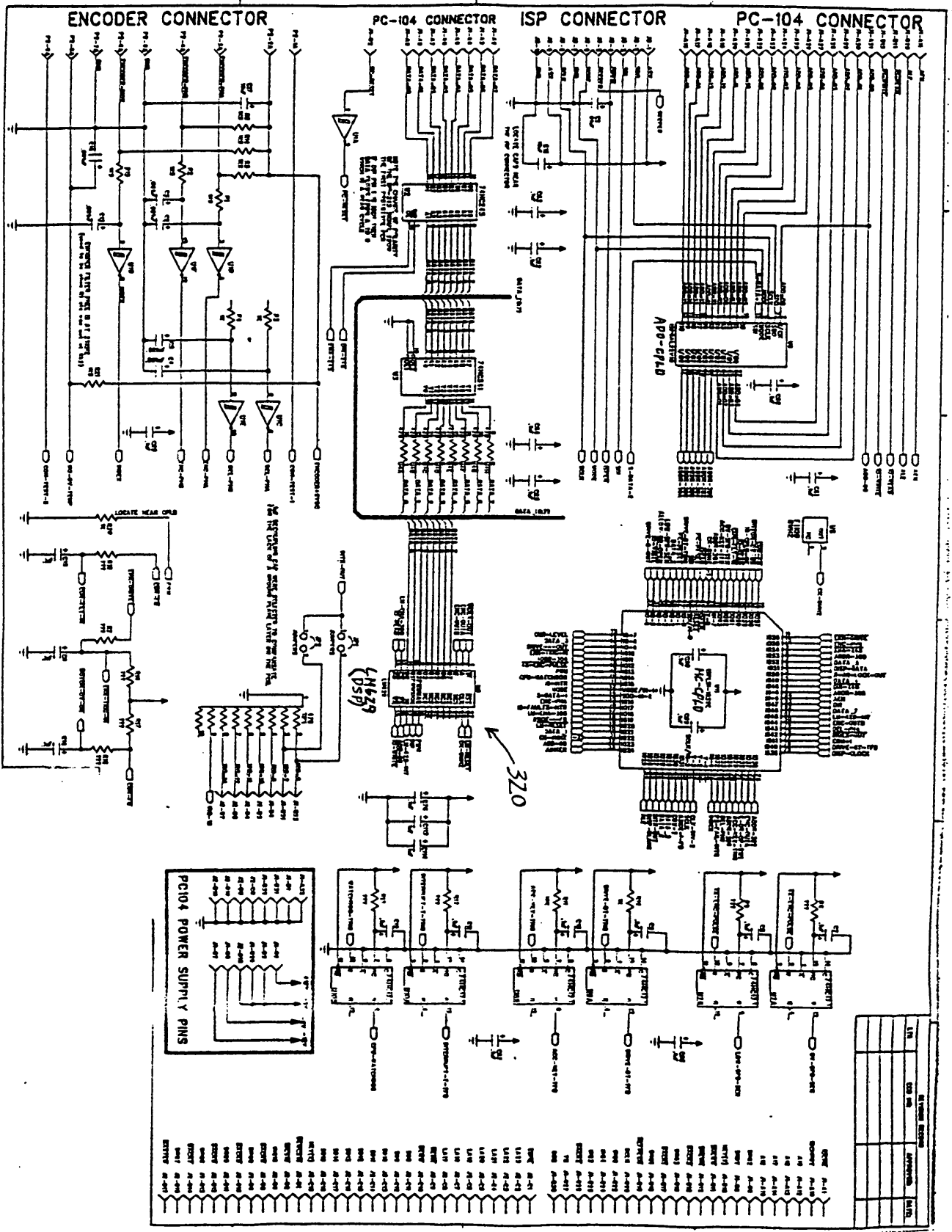
FIGURE 9



MD-I/O CARD
LAYOUT 1

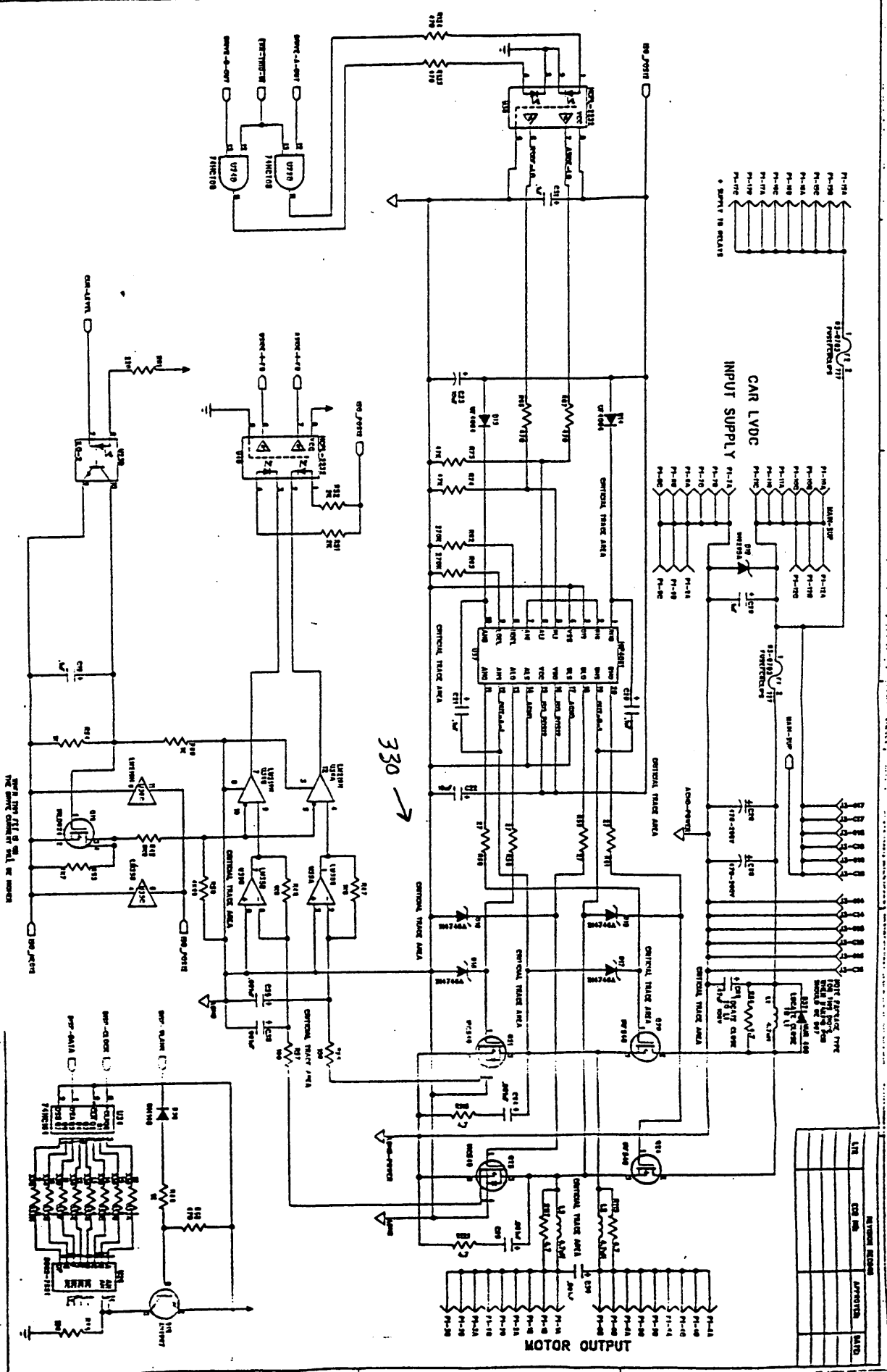
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FIGURE 10B



MD-IPb CARD
LAYOUT

FIGURE 10C



MD-I/O CARD
Layout 3

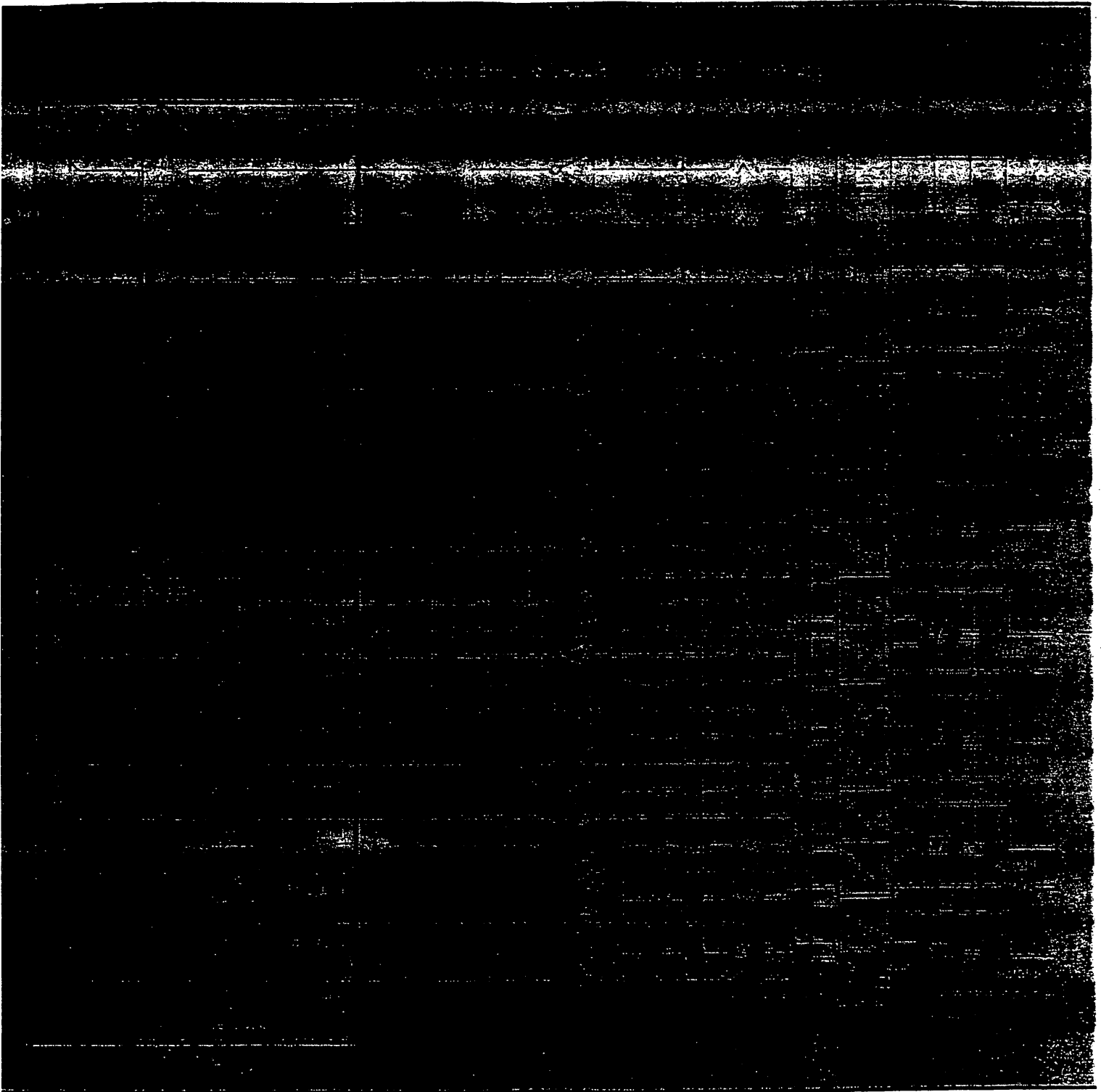
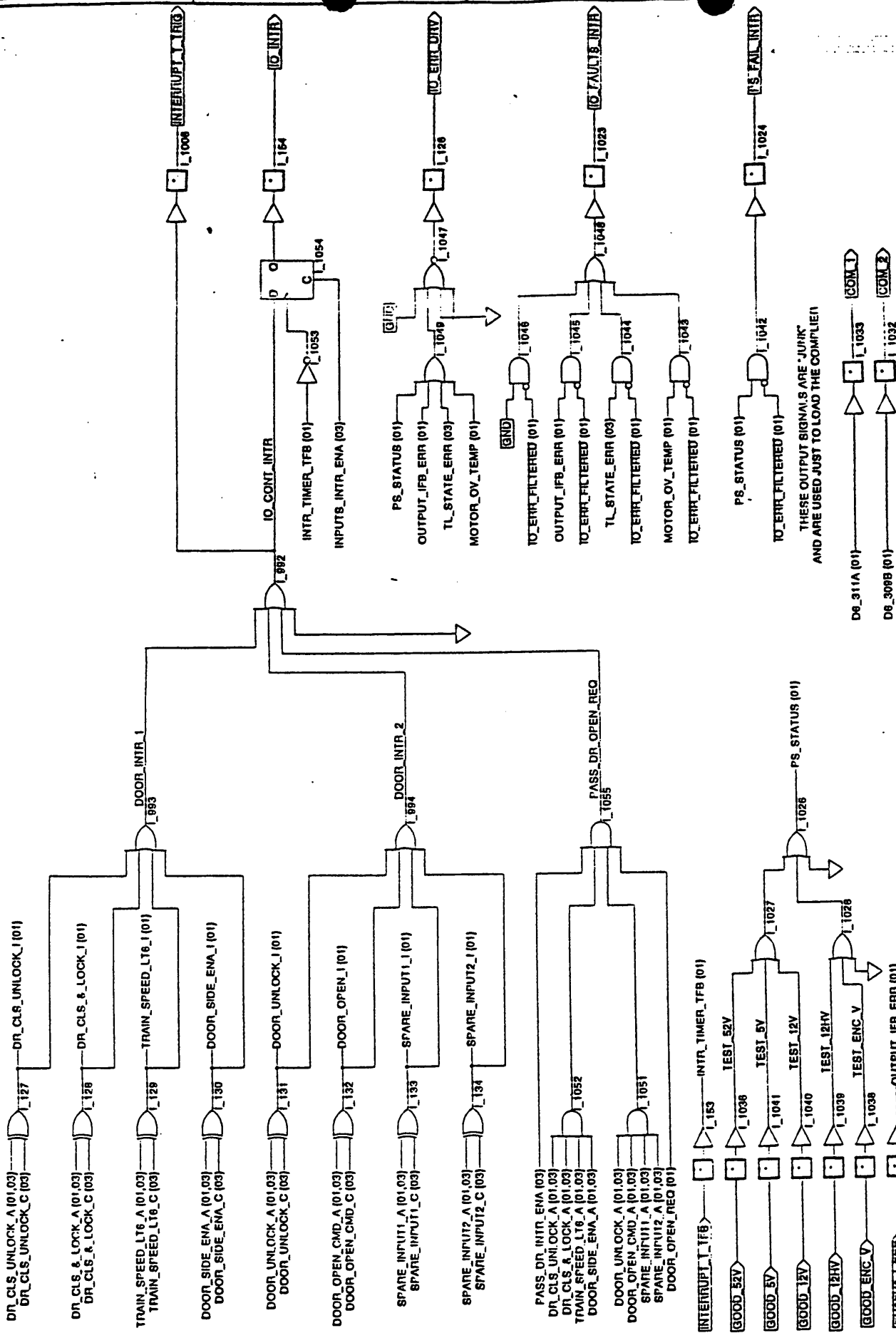


FIGURE 11

↑
310

INTERRUPT AND FILTER CONTROL CIRCUITS



MD-I/O CARD
(I/O-CPU0 INTERRUPT AND
FILTER CONTROL CIRCUITS)

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FIGURE 12C

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FIGURE 12B

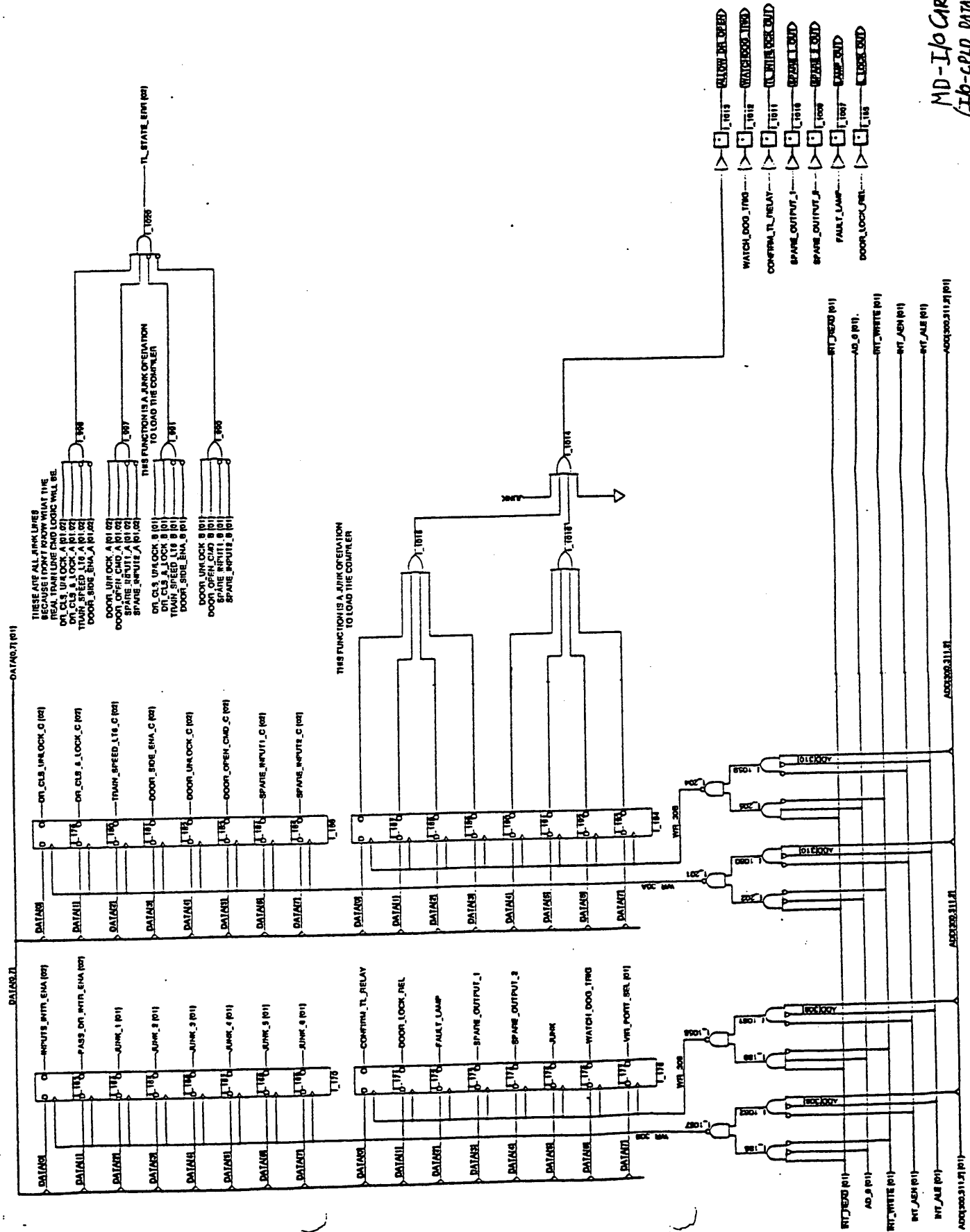
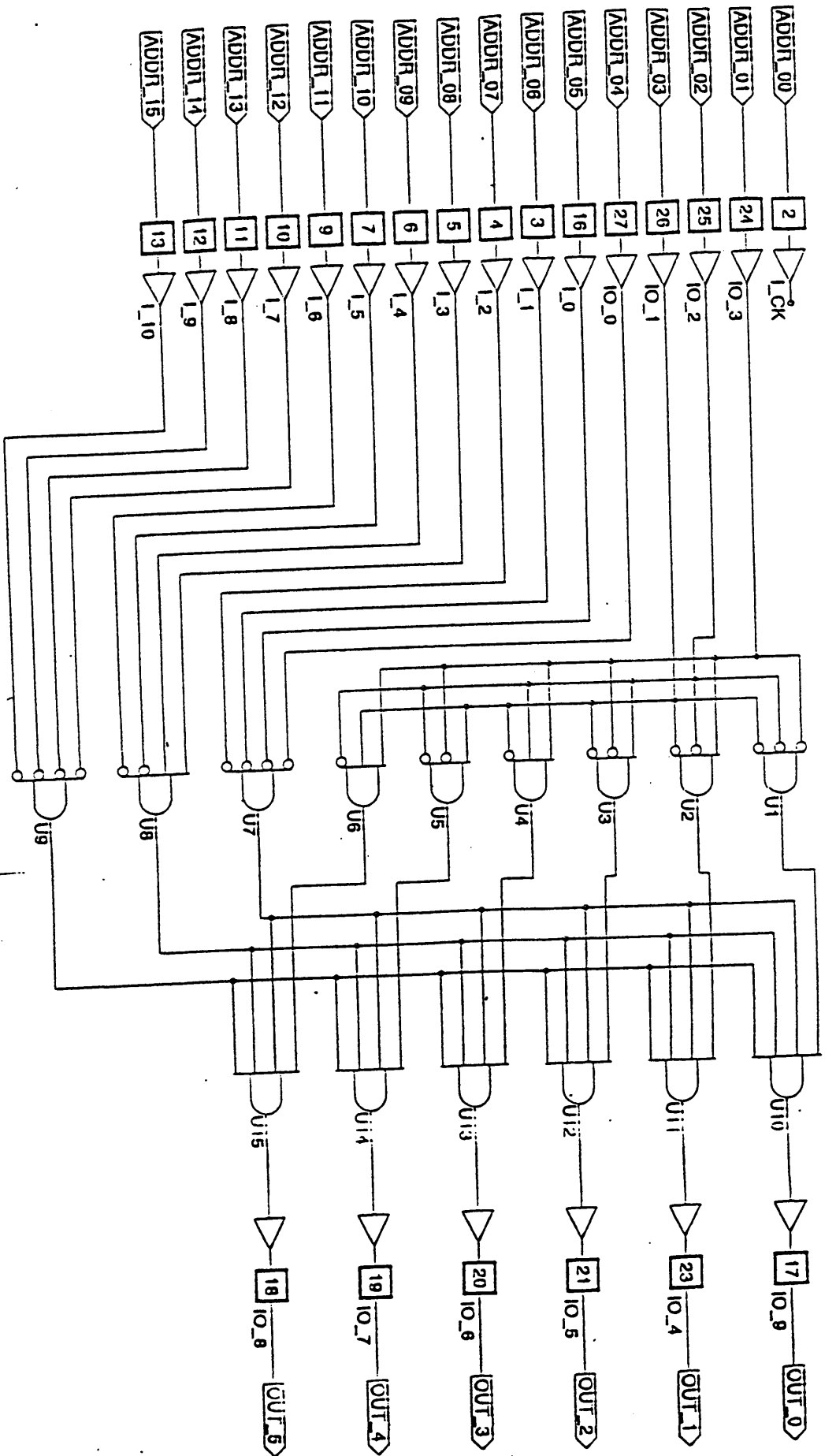


FIGURE 13



MD-I/O CARD
(Add-CPLD Logic)

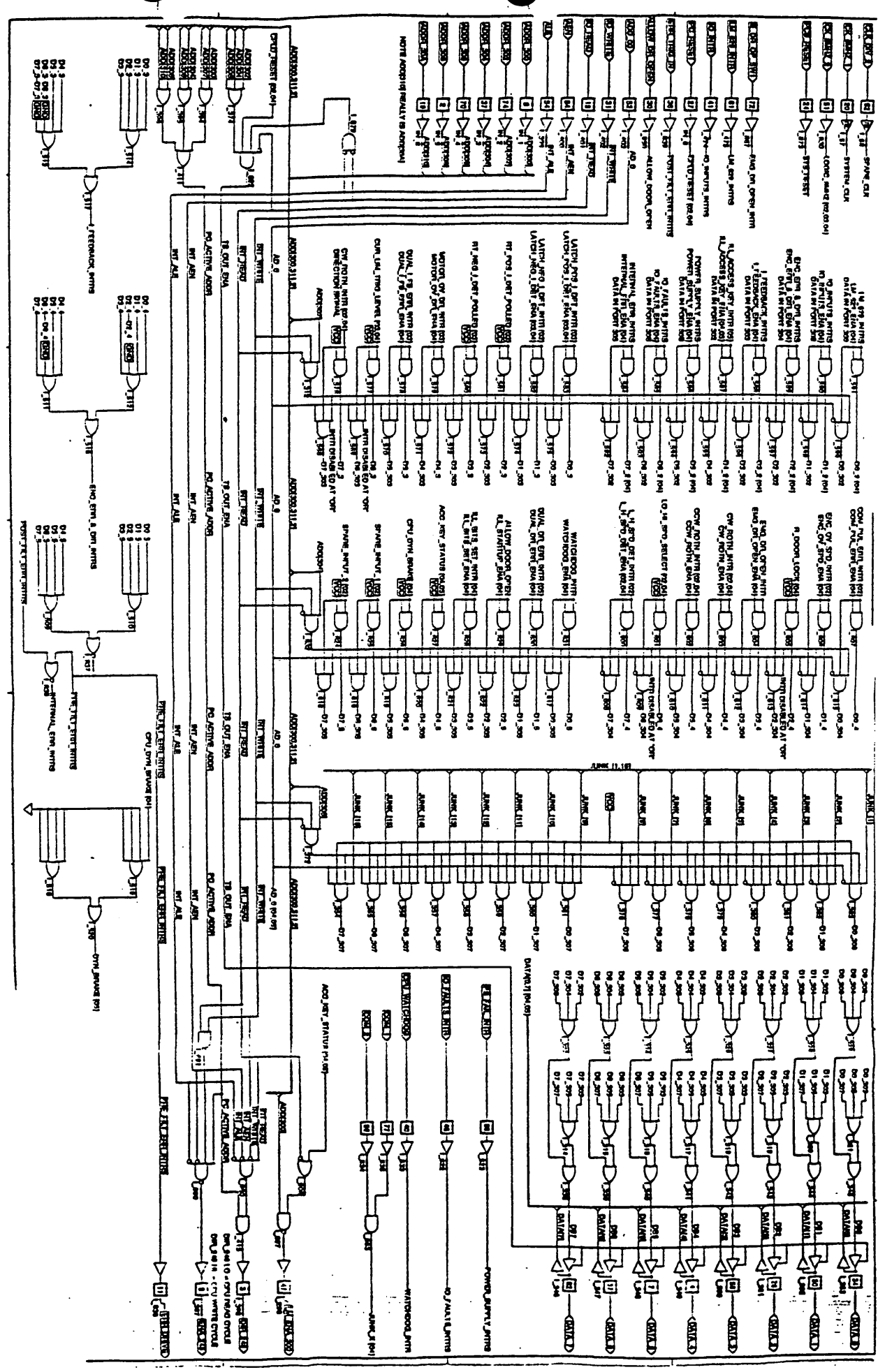


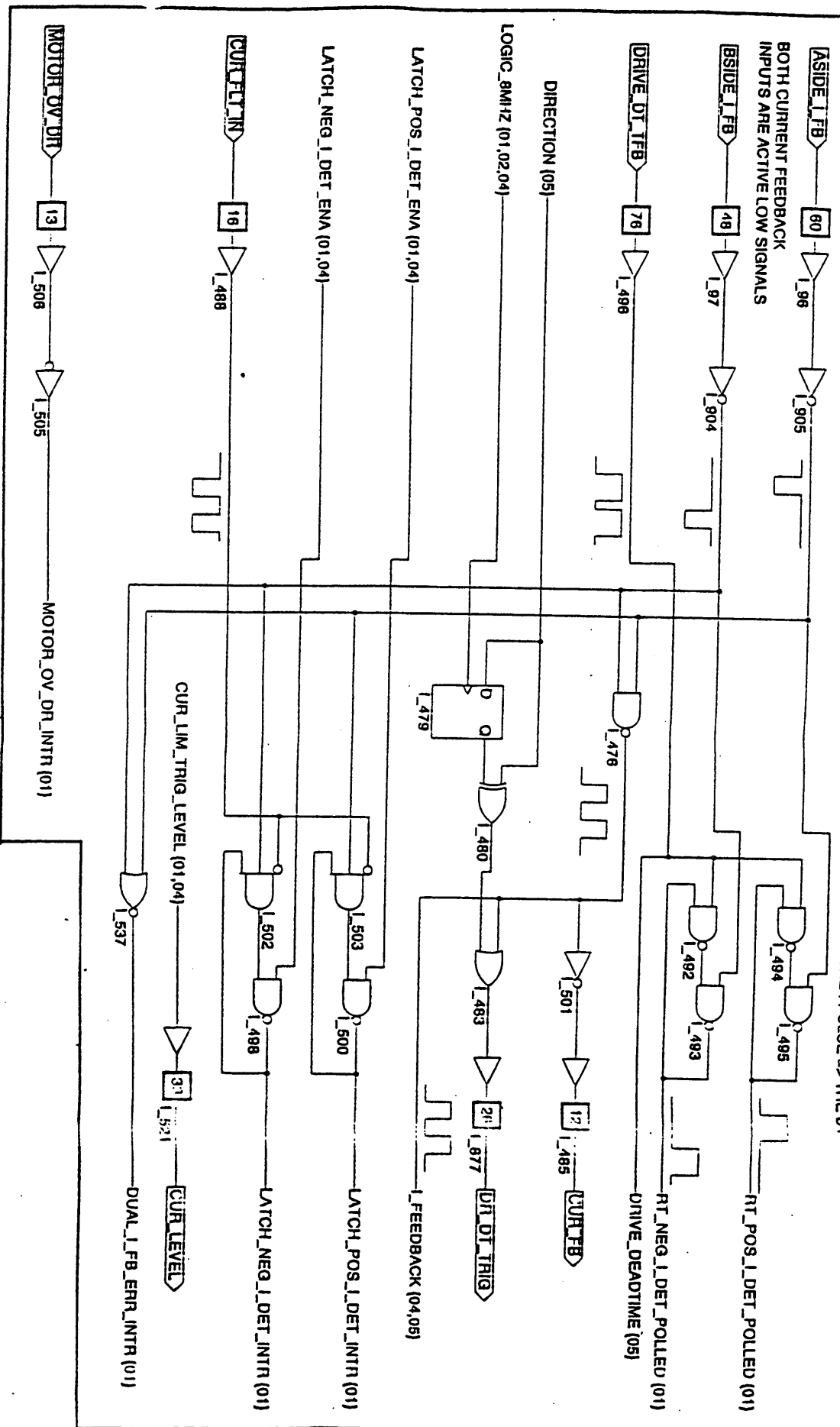
FIGURE 14A

MD-1/0 CAPD
(Mc-CPLD DATA/ADDRESS)
CONTROL LOGIC

FIGURE 14B

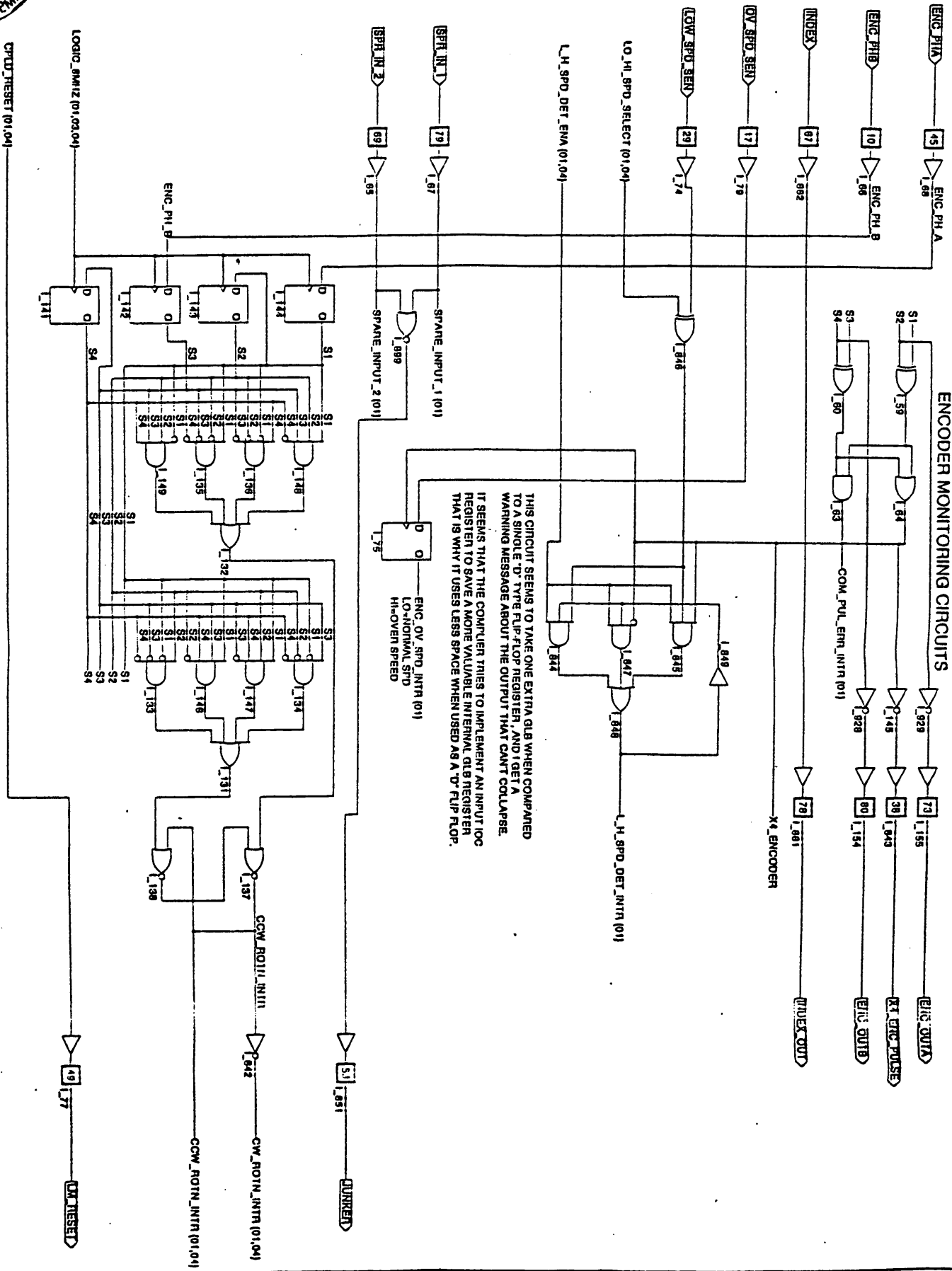
CURRENT FEEDBACK & DEADTIME CIRCUITS

THIS IS A CURRENT PULSE STRETCHER CIRCUIT THAT WILL ALLOW THE I PULSE => THE DI



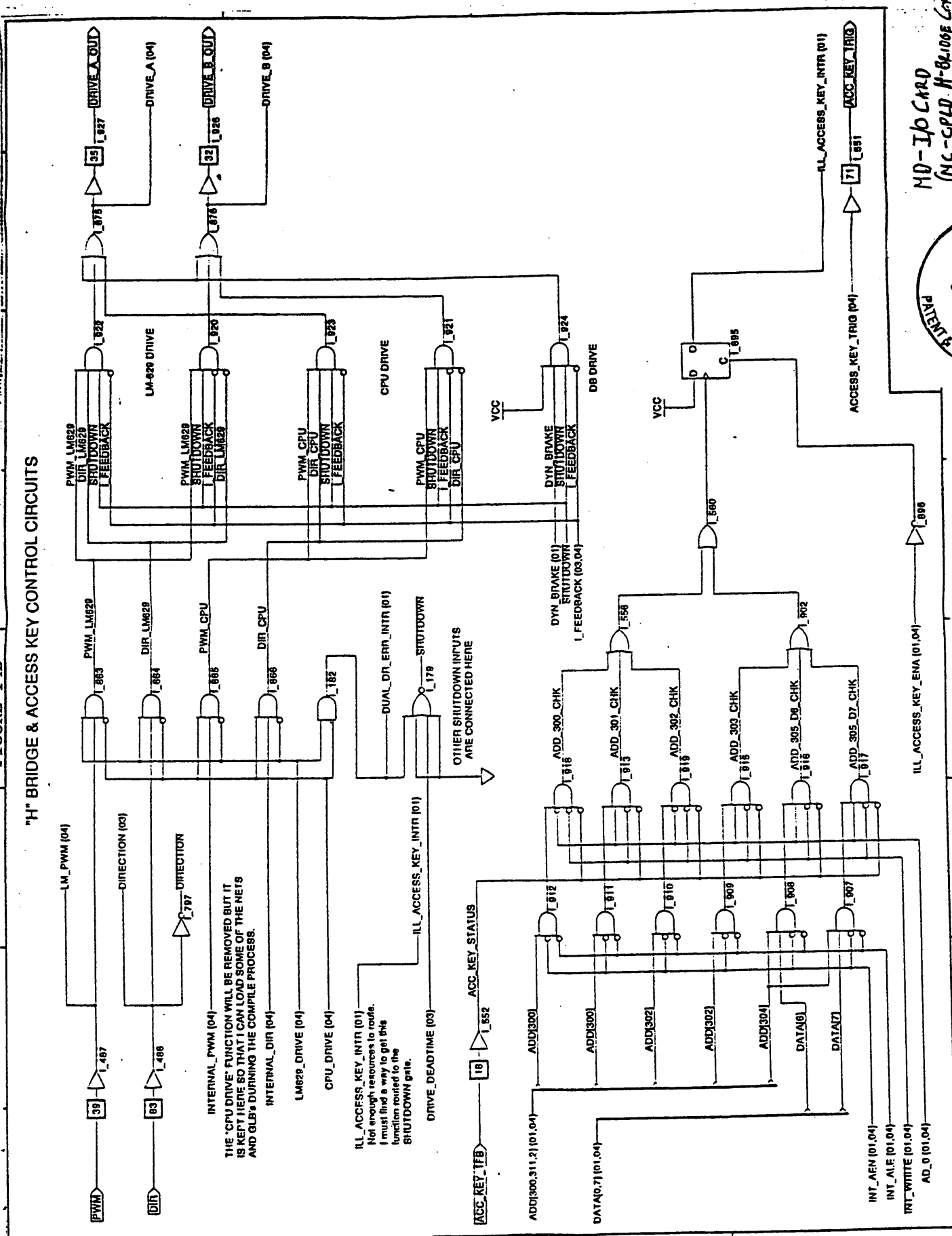
MD-I/O CARD
(MC-CPLD CURRENT FEEDBACK
AND DEADTIME CIRCUITS)

ENCODER MONITORING CIRCUITS



MD-10 CARD
(MC-CPLD ENCODER
Monitoring Circuits)

FIGURE 14D

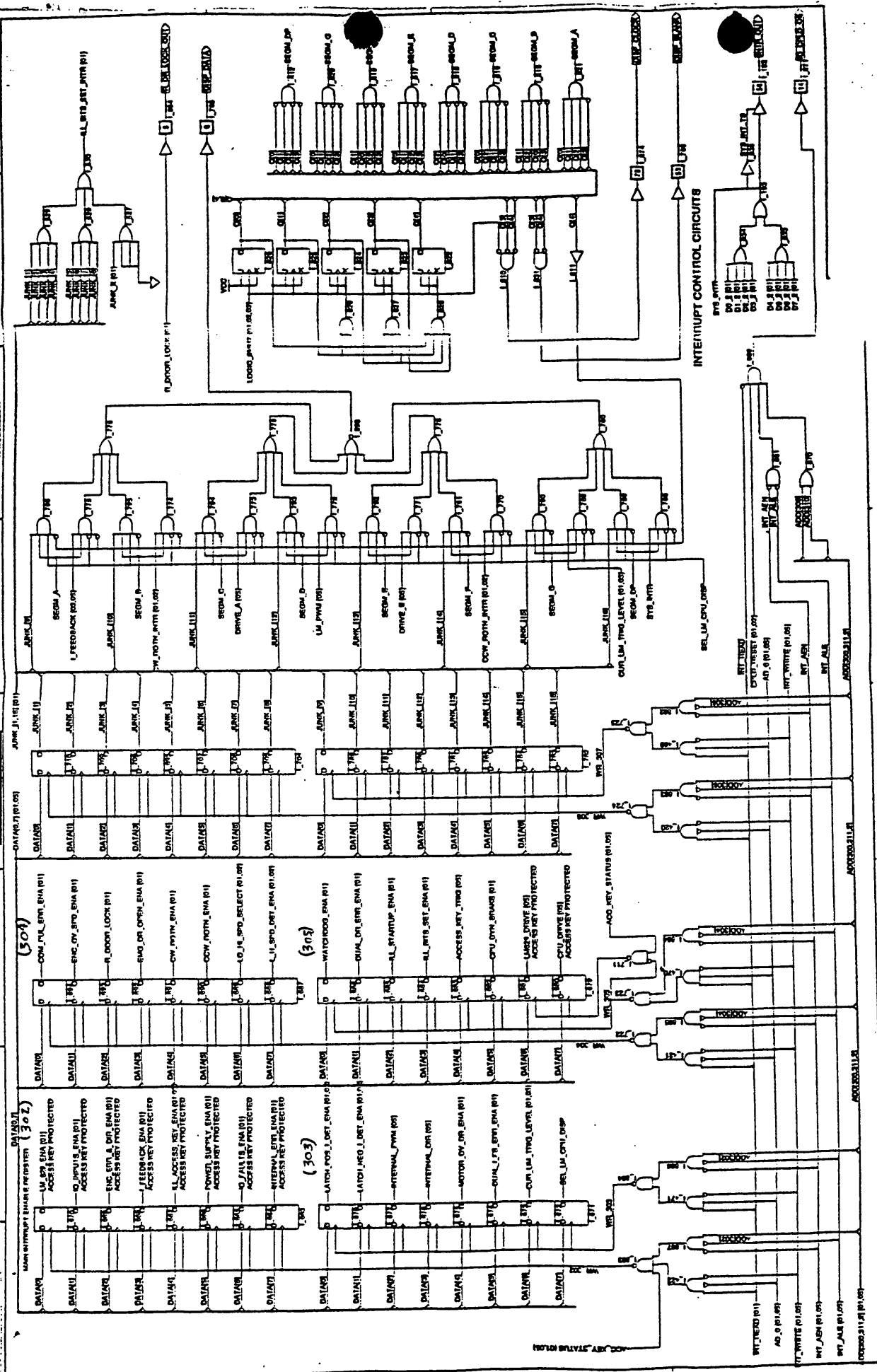


MD-I/O CARD
(MC-CPLD, H-BE1006 CONTROL)
(AND ACCESS KEY CONTROL)

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FIGURE 14E



MD-16 CARD
(MC-CPLD INTERRUPT)
CONTRA LOGIC



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